Organized by
Electronics & ICT Academy

MNIT Jaipur
http://www.mnit.ac.in/eict

A refresher course on

Modelling and Simulations of Custom Power Devices for Power Quality Improvement

November 19th -23rd, 2018
Venue: Academy, Prabha Bhawan, MNIT Jaipur
http://academymnit.wordpress.com

Faculty Development Programme
Sponsored by

Department of Electronics & Information Technology
Ministry of Communications & Information Technology
Government of India

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Government of India
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Preamble (Electronics & ICT Academy)
Government of India had announced a National Policy on Skill Development, which has set a target of skilling 500 million people by 2022 in the domain of Electronics & IT. Under the plan scheme of "Digital India Manpower Development". MeitY has set up seven (07) Electronics and ICT Academies as a unit in 03 IITs, 03 NITs and 01 IIIT with an objective of faculty/mentor development/up gradation in the areas related to Electronics & ICT leading ultimately to improved employability of graduates/diploma holders. MNIT Jaipur has set up such an academy for providing specialized training to faculty and industry persons in the states/UTs of Rajasthan, Gujarat, Daman & Diu, Dadra Nagar Haveli.

(A) Issues-
1. IT Hardware and Electronics Manufacturing industry- availability of properly trained, skilled and qualified manpower
2. Number of quality PhDs generated in IT / Computer Science is very low
3. In E & ICT domain- there is a very high degree of obsolescence of existing technologies and faster emergence of newer technologies

(B) Approach-
1. A focused faculty training/updation programme for IT, Electronics and related sectors
2. Spreading up and continuous updation regarding Emerging Technology
3. Training and consultancy services for Industry
4. Design, Develop and Deliver specialized modules for specific research areas and Industry
5. Providing advice and support for technical incubation and entrepreneurial activities

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An intensive theoretical, simulation-based and practical oriented one-week training programme on power quality improvement is being organized for faculty of engineering and technological institutions. It is also open to persons from industry and doctoral students of Indian organizations. The objective is to provide a simulation and experimental exposure to the participants about the recent trends of Custom Power Devices for Power Quality Improvements through interaction with experts from academic CFTI institutions such as IITs/NITs/IIITs including host institution. The technical program will include state-of-the- art lectures, hands-on lab sessions, experimental demonstrations, and discussion/presentation sessions.

Programme topics
A. Electric Power Quality: Monitoring, Analysis, Standards and Codes
B. Addressing Power Quality Issues due to Power Electronics Converters
C. State-of-the-art of Custom Power Devices
D. Power Quality and Passive and Active Filtering Techniques
E. Principle, Modelling and Control of Active Power Filter, DSTATCOM, UPQC
F. Pulse Width Modulation Techniques
G. Role of Simulating Software in the Analysis of Custom Power Devices
H. Demonstrations of High Speed Processor like dSPACE/DSP/FPGA in Custom Power Devices,
I. Challenges and Opportunities in Filtering Techniques

Invited Experts –
1. Prof. Bhim Singh, IIT Delhi, 2. Prof. Kishore Chatterjee, IIT Mumbai
3. Prof. Pramod Agarwal, IIT Roorkee, 4. Dr. K. Ragavan, IIT Gandhinagar & Other Experts from IIT/NIT and industries

Programme coordinators:
Prof. R. A. Gupta   ragupta.ee@mnit.ac.in   9549654479 (M)
Dr. Nitin Gupta   nitingupta.ee@mnit.ac.in   9549658136 (M)

Registration:
Registration is open to faculty, industry persons, doctoral and postgraduate students of programmmes related to Electronics and Communication/ Electrical Engineering/ Computer Science/Information Technologies. Participants will be admitted on a first-come first-served basis.
Register on line at http://www.mnit.ac.in/eict/acad_training_prg.php

Fee:
(A). The one-time registration fee of Rs. 500/- is to be paid by each participant attending first time, irrespective of affiliation. This fee is not applicable for those participants, who have attended any academy training programme earlier.
(B). (i) The participants from academia and research scholars are required to pay a further fee of Rs. 2000/- (faculty/research-scholars). Rest expenditure is sponsored by MeitY through Electronics & ICT Academy at MNIT Jaipur.
    (ii) The participants from industries, UG/PG students are required to pay a further fee of Rs. 5000/-.
    (iii) Applicable relaxation for SC/ST candidate is 50%.
    (iv) Fee once paid will not be refunded back, however, it may be adjusted to another FDP.
(C) The fee covers the participation in the programme, registration material including tutorial notes, boarding (breakfast/lunch) on all the days of the workshop. The travel and other expenses would have to be borne by the participants or their parent-organizations.
(D) Complimentary lodging for a very limited number of participants is available in Guest rooms of Aurobindo hostel. Also, accommodation in Guest-house-2 can be arranged on first-come first-serve and additional payment basis.
(E) The organizers should receive the registration amount through online payment/NEFT/IMPS.

Account Name- 'Electronics and ICT Academy MNIT Jaipur'	Account Number- 676801700483
Bank address- ICICI Bank, MNIT Campus Branch, Jaipur	IFSC Code- ICIC0006768

(F). Please pre-intimate your desire to participate and for accommodation to programme coordinator through e-mail, immediately after online registration.
→For any other query else then this FDP, **email us at** academy@mnit.ac.in